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Patent Application No. 10/826,899
Customer Number: 42717

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Syun-Ming Jang, et al.
Serial No.: 10/826,899
Filing Date: April 16, 2004
For: Logic Embedded-Memory
Integrated Circuits

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Docket No.: 24061.231
(TSMC2003-1383)
Examiner: Calvin Lee
Art Unit: 2818
Conf. No.: 2231

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop: Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed February 4, 2005, applicant hereby elects, Group I, Claims 1-34, which is drawn to a semiconductor device.

Applicant's election is made with traverse on the grounds that the embodiments delineated by the examiner are not patentably distinct and therefore constitute a single invention concept.

An early action on the merits is respectfully requested.

Respectfully submitted,

David M. O'Dell
Reg. No. 42,044

Date: 2-24-05

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop: Amendment, Commissioner For Patents, PO Box 1450, Alexandria, VA 22313-1450 on the date below.

Name

Date

2-24-05